

DEC 08 2005

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FORM PTO-1449

## INFORMATION DISCLOSURE STATEMENT

ATTY. DCKET NO.  
1778, 0000002 (0055.20US)APPLICATION NO.  
09/662,832APPLICANT  
Van Hook et al.FILING DATE  
September 15, 2000GROUP  
2183

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
2	AA1	3,654,621	04/1972	Bock et al.	340	172.5	
2	AB1	3,916,388	10/1975	Shimp et al.	340	172.5	
2	AC1	4,023,023	05/1977	Bourrez et al.	235	152	
2	AD1	4,109,310	08/1978	England et al.	364	200	
2	AE1	4,128,880	12/1978	Cray, Jr.	364	200	
2	AF1	4,130,880	12/1978	Nutter	364	900	
2	AG1	4,219,874	08/1980	Gusev et al.	364	200	
2	AH1	4,317,170	02/1982	Wada et al.	364	200	
2	AI1	4,396,982	08/1983	Wada et al.	364	200	

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
2	AJ1	WO 97/07450 A1	02/1997	PT	G06F	7/00	N/A
2	AK1	WO 97/08608 A1	03/1997	PC	G06F	7/00	N/A
2	AL1	0 681 236 A1	11/1995	EP	G06F	9/302	N/A
2	AM1	0 681 236 B1	11/2000	EP	G06F	9/302	N/A

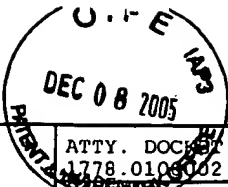
## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

2	AN	1	An Introduction to Thumb™, Version 2.0, Advanced RISC Machines, 29 pages (March 1995).
2	AO	1	Bier, J., "DSP16xxx Targets Communications Apps," <i>Microprocessor Report</i> , MicroDesign Resources, pp. 11-15 (September 15, 1997).
2	AP	1	Case, B., "Superscalar Techniques: SuperSPARC vs. 88110," <i>Microprocessor Report</i> , MicroDesign Resources, Vol. 5, No. 22, pp. 1 and 6-11 (December 4, 1991).
2	AQ	1	Diefendorff, K., "88410 Second Level Cache," <i>Microprocessor Forum</i> , Motorola Inc., 20 pages (November 1991).
2	AR	1	Diefendorff, K. and Allend, M., "Organization of the Motorola 88110: A Superscalar RISC Microprocessor," 12 pages, Source and date unknown.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.



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## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
<i>h</i>	AA2	4,491,910	01/1985	Caudel et al.	364	200	
<i>h</i>	AB2	4,507,731	03/1985	Morrison	364	200	
<i>h</i>	AC2	4,520,439	05/1985	Liepa	364	200	
<i>h</i>	AD2	4,583,199	04/1986	Boothroyd et al.	364	900	
<i>h</i>	AE2	4,773,006	09/1988	Kinoshita et al.	364	200	
<i>h</i>	AF2	4,814,976	03/1989	Hansen et al.	364	200	
<i>h</i>	AG2	5,150,290	09/1992	Hunt	364	736	
<i>h</i>	AH2	5,418,915	05/1995	Matuda et al.	395	325	
<i>h</i>	AI2	5,513,366	04/1996	Agarwal et al.	395	800	

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
	AJ2						Yes No
	AK2						Yes No
	AL2						Yes No
	AM2						Yes No

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

<i>h</i>	AN	2	Fiegel, C.P., "TI Introduces Four-Processor DSP Chip," <i>Microprocessor Report</i> , MicroDesign Resources, pp. 22-25 (March 28, 1994).
<i>h</i>	AO	2	Gwennap, L., "Digital, MIPS Add Multimedia Extensions," <i>Microprocessor Report</i> , MicroDesign Resources, Vol. 10, No. 15, pp. 1-5 (November 18, 1996).
<i>h</i>	AP	2	Gwennap, L., "New Multimedia Chips to Enter the Fray," <i>Microprocessor Report</i> , MicroDesign Resources, Vol. 10, No. 13, 1 page (October 1996).
<i>h</i>	AQ	2	Hansen, C., "Architecture of a Broadband Mediaprocessor," <i>COMPCON 96</i> , IEE, pp. 1-8 (February 25-29, 1996).
<i>h</i>	AR	2	DSP56002 24-Bit Digital Signal Processor User's Manual, Motorola, Inc., 386 pages (1995). (Bates Numbers L07277-L07662).

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